

Linux Windows;

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**ПРОГРАММНАЯ ПОДДЕРЖКА
И ПЛАНИРОВАНИЕ ЗАДАЧ
В СИСТЕМАХ РЕАЛЬНОГО ВРЕМЕНИ**

() -
[1, 2]. -
, -
, -
, -
() -
() -
() [3]. -
1.0 · 10⁻⁶ -
1.0 · 10⁴ . -
, / -
(), -
- -
Linux Win-dows; -
, ; -
; Linux Win-dows; -
; -
; -
() -
-
.

[5].

POSIX (Portable operating system interface for computer environments, IEEE 1003.1) Unix-
 POSIX (>30) (1003.1a-d/1j /21 /2h).
 , TRON (the RTOS Nucleus,)

653 () ED-12B (). DO-178B, ARINC-
 OSEK/VDX (OSEK),

[1].
 POSIX

Definition (IEEE Std 1003.n, : 1003.1a/OS
); 1003.1b /Realtime extensions (: Sched_fifo – fifo;
 Sched_rr /Round robin – ; Sched_other –
); 003.1c/Threads ();
 1003.1d (; 1003.21 ();
 1003.2h () [4].

DO-178A/B/C RTCA (Radio technical commission for aero-
 nautics)
 : – , – , – , D – ,
 –

ARINC-653 (Avionics application software standard interface)
 APEX (Application/ Executive)

(partitioning) [6].

OSEK/VDX (OSEK –
 (), VDX (Vehicle distributed eXecutive) – PSA
 Renault (). OSEK/VDX
 API
 . OSEK/VDX : /OS;
 (COM); (NM).
 (OIL). OSEK [7].

SCEPTRE (Standardisation du coeur des executifs des produits temps reel europeens)

[8]. [9]. (IRQ); 10 ();

TCSEC (Trusted computer system evaluation criteria) –

«Orange book», : 1 – ; 3 – ; 2 – ; 2 – ; 1 – ; D – « (Common criteria

for IT security evaluation, ISO/IEC 15408).

«Common criteria ...» (Evaluation assurance levels – Eal): Eal7 () ; Eal6 – ; Eal5 – ; Eal4 – ; Eal3 – (Eal2); Eal2 – ; Eal1 –

«Common Criteria ...», /

5 – 7

Eal4

, «Common criteria ...»

• POSIX
 ;
 ; API;
 • ();
 • / (, TCP/IP) ;
 (/); (,) ;
 • ;
 IRQ:
 ISR (interrupt service routine),
 handling routing); IHR (interrupt
 , Linux – upper/bottom half
 (/), «bottom» IHR, «upper» –
 ISR. W's DPC, IHR; (DPC).
 • / (32,
 POSIX); fifo rr (round robin); / EDF
 ((– , –);
 • ;
 • Host-Target /Self-Hosted.
 Self-Hosted –
 , , , , – ,
 , , , , ,
 • Host-Target – (host), / (target),
 Ethernet, compact PCI . . . host
 Unix/W's, target – /
 () , –
 : - ,
 . . .

self-hosted host/target

(target); host-target; (host);

LynxOS; OS-9; pSOS; RTC; VRTX; VxWorks; QNX [10].

POSIX 1003; LynxOS (Lynx RTS): self-hosted; ; POSIX 1003; - 255;

80x86, Motorola 68xxx, SPARC, PowerPC; / ; preemptive ; ; Intel

256/ 124/ 33 K ; POSIX 1003 (, mutex, condvar);

(/ ++, , - 28 , Lyux; Total View - kernel

plug-ins, Unix

/ : , -

Sony, Sagem , TGV (). Linux -

• POSIX 1003.: 1 - (thread); 1b - - ;

• VME; VMEPCI, Linux , ;

• Unix , preemption , IRQ , Linux, (nonpreemptive) [11]. preemption Linux:

- , -

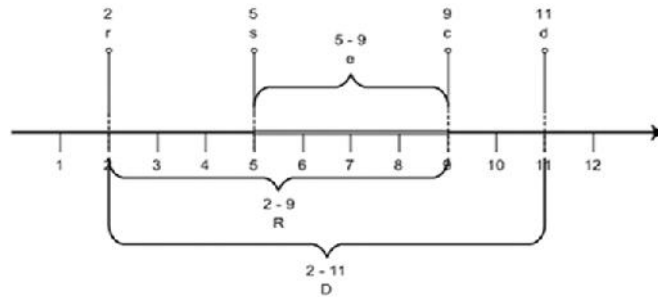
- ; preemption , Linux -

W's , , RT-Linux (W's 2,7 M). -

Win32, W's, -

, W'nt
 :
 (16 – 32 (ISR DPC),
). W'nt IRQ
 ; ; , .),
 .
 1. W's
 «Radisys», W'nt VxWorks «LP Elektroniks»
 InTime
 , W's
 2. W's
 «VenturCom» RTX
 W'nt (HAL – hardware abstraction layer,
 HAL
 (RTAPI) IRQ. RTX
 W's,
 RTAPI,
 W's (,).
 :
 ; ; ().
 (,) ;
 , ,
 .
 [12].
 ,
 ,
 ; [13, 14].
 (t): r (release time) –

(r); d (absolute deadline/); s (start time) –
 ; (completion time) –
 ; $D = d - r$ (relative deadline) –
 ; $e = c - s$ (execution time) –
 ; $R = c - r$ (response time) –
 . 1
 $r = 2, d = 11, s = 5, c = 9, D = 11 - 2 = 9, e = 9 - 5 = 4, R = 9 - 2 = 7.$



. 1. T_j

(d, D)

$\{T_j\}$ – ; $r_{jk} - t -$ T_j ;
 $T_j[\Phi_j, p_j, e_j, D_j] -$, $r_{j1} -$ Φ_j ;
 p_j D_j ; $p_j = D_j$; $p_j > D_j$; $p_j < D_j$ (
 $\exists: \{\bar{T}_j\}$ $varT_j$).

$$T_j : u_j = \frac{e_j}{p_j},$$

$$\{T_j\} - U = \sum u_j = \frac{\sum e_j}{p_j} [15].$$

$corH$, $allD_j$. $H -$, $\{T_j\}$ –
 $corH$ $\{T_j\}$.
 $\forall \{T_j\} \exists H$, $corH$.

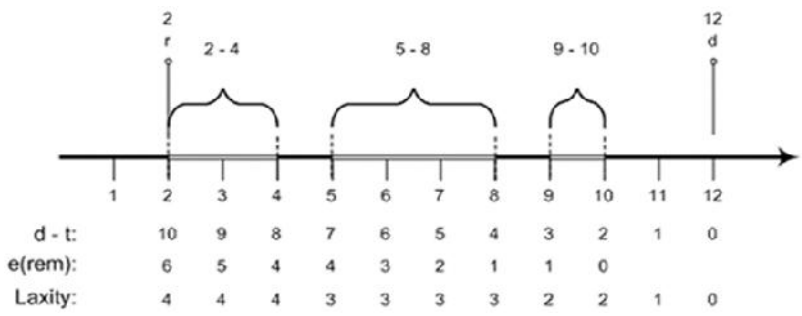
$\{T_j\}$,
 $H\{t_j = \phi[T_j]\}$,
 H
 $p = lcm_j\{p_j\}$.
 $\{T_j\}_{j=1}^4 : T_1[* , 4 , * , 1] : T_2[* , 5 , * , 1.8] :$
 $: T_a[* , 20 , * , 2]$ $P = lcm\{4, 5, 20, 20\} = 20$.
 $T_2 - 4, T_a - T_4 -$
 $allD_j$.
 $l - \ll \gg$ (Idle task).

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
t_j	0	1	2	3.8	4	5	6	8	9.8	10.8	12	13.8	14.8	16	17	18	19.8
\bar{t}_j	0.2	1	1	1.8	0.2	1	1	2	1.8	1	1.2	1.8	1	1.2	1	1	1.8
	T1	T3	T2	I	T1	I	T4	T2	T1	I	T2	T1	I	T1	I	T2	I

t IRQ
 Δt
 t, t_j
 $t_j = t_{j+1} - t_j$.
 $\ll \gg / \ll \gg$;
 $H\{t_j = \phi[T_j]\}$;
 $\dim H\{t_j = \phi[T_j]\}$.

- EDF (earliest deadline first) – $T_i : D_i = \min[D_j]$
 - LLF (least laxity first) – $T_i : L(t)_i = \min[e(rem)_j]$
- EDF/LLF $\forall\{T_j\}$ (p_j, D_j), $allT_j :$

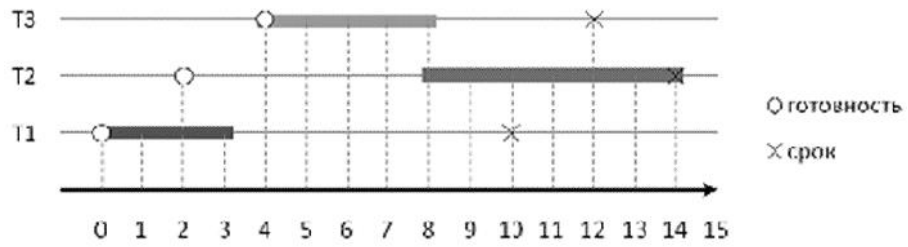
$\forall \{T_j\} \exists corH,$
 $\bar{H} \quad cor\bar{H}, \quad t_j = \phi[T_j]$
 EDF / LLF.
 / (laxity)
 $L(t) = (d-t) - e^{(rem)}$
 $t=2 \quad t=12.$
 $T_j \quad L(t) = \text{const}, \quad D_j$
 $T_j, \quad D_j$
 $L(t)$



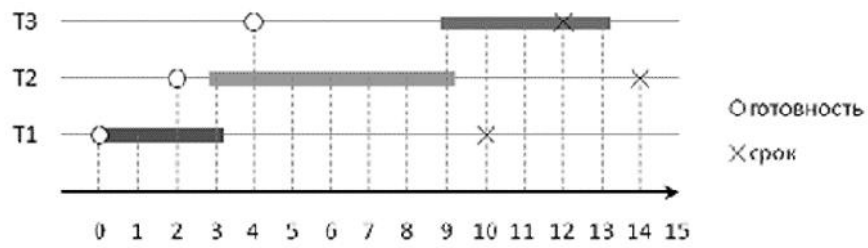
.2. (laxity)
 $[\{T\}]_j,$
 $\exists corH, \quad \neg corH.$

$T_j[r, e, d]: T_1[0, 3, 10]; T_2[2, 6, 14]; T_3[4, 4, 12], \quad , \quad , \quad corH \quad (.3).$

$H (\quad , \quad)$
 $t, \quad T_j$
 T_j
 $t=3 \quad T_2,$
 $T=4 \quad T_a,$
 $T_2 \quad T_a$

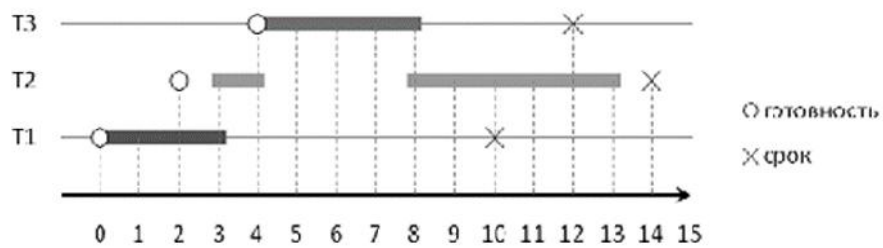


.3. $corH \quad \{T_j\}_{j=1}^a$



.4. $\{T_j\}_{j=1}^a$

$[(T)]_j$. $H,$ $. 5 \text{ all } T_j$ $D_j, \dots t=2$
 $T_2,$ $T_1,$ T_2 $t=3.$ $t=4.$
 T_a $T_2,$ T_a T_a $T_2.$
 $t=8,$ T_a $T_2.$



.5. $\{T_j\}_{j=1}^a$

$,$ $,$ $($ $,$ $)$
 $,$ $,$ $e.$

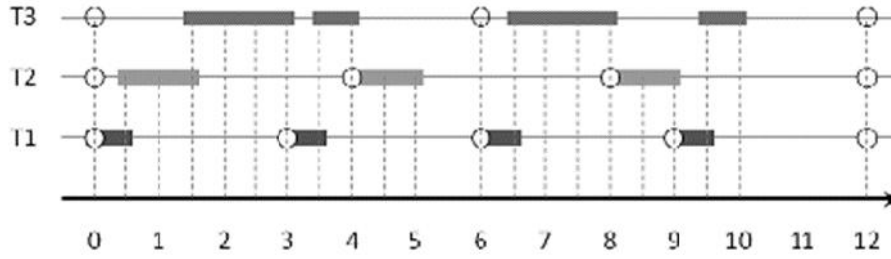
$\{T_j\}$ $H\{t_j = \phi[T_j]\}$.
 $\{T_j\}$ (-
 $\forall_j : D_j \gg p_j,$ EDF- -
 $U \leq 1.$,
 $D_j \leq p_j,$,
 $T_j(p, e, D) : T_1(2;1;1,9);$
 $T_2(2;1;1,9)$ -
 $U = 1.$ « -
 \gg « »,
 $\delta = \frac{e}{\min(D, p)}.$ $\{T_j\}$ -
 $all T_j : \Delta = \sum \delta_j = \frac{\sum e_j}{\min(p_j, D_j)}.$ *corH*
 $\{T_j\}$
 $\forall_j : D_j < p_j,$ EDF- ,
 $\Delta < 1.$ *corH*;
 $\exists corH.$ $T_1(2;0,6;1)$
 $T_2(5;2,3) \quad \Delta \leq 1 \quad (= 0,6/1 + 2,3/5 = 1,06 > 1)$,
 $\exists corH.$

\vdots

- RMS (rate monotonic scheduling) –
 $T_i : D_i = \min[p_j] / \text{rate } T_i$;
- DMS (deadline monotonic scheduling) –
 $T_i : D_i = \min[D_j].$

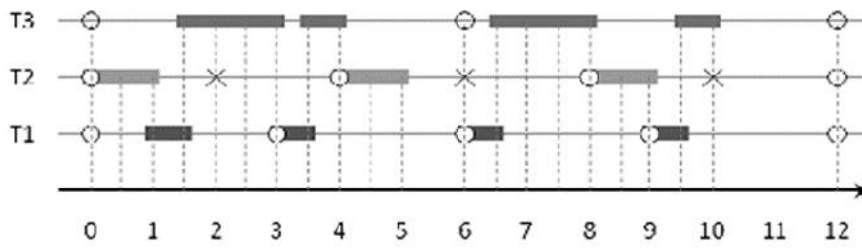
$\cdot 6 \quad H_{RMS} \quad \{T_j\}_{j=1}^2$
 $: T_1(3;0,5); T_2(4;1); T_2(6;2).$

$T_1, \quad t=0$
 $T_1, \quad t=0,5 \quad - T_2, \quad t=0$
 $(T_2$
 $T_3), \quad T_2, \quad t=3 \quad T_1, \quad T_2$
 $t=3,5 \quad T_a$
 $t=4,0 \quad T_2, \quad t=6$
 $(T_1 \quad T_a), \quad T_1, \quad T_a, \quad t=8$
 $T_2, \quad t=9 \quad T_1, \quad T_a, \quad t=9,5$
 $T_a.$



C. 6. $H_{RMS} \{T_j\}_{j=1}^2$

$H_{DMS} (\dots 7)$:
 $T_1(3;0,5); T_2(4;1,2); T_a(6;2)$,
 T_2 . T_2 ,
 $- T_a$.



C. 7. $H_{DMS} \{T_j\}_{j=1}^a$

() : DMS RMS .
 $\{T_j\}$,
 $\forall T_j T_k p_i < p_k$, $p_k \bmod p_i = 0$.
 $all T_j$, $\{T_1(2,1), T_2(4,1), T_a(8,11)\} -$
 $\{T_1(2,1), T_2(5,11), T_a(10,1)\}$
 () .
 $\{T_j\}$ (-
 $\{T_j\}$ -
 $p_j < D_j$, $U < 1$.
 p_j -
 (/ RMS) .

$\{T_j\}_n$, $p_j = D_j$,
 $U \leq n \left(\frac{21}{n} - 1 \right)$,
 ;
 ;
 Linux Windows; , -
 ; / ; -
 [16, 17] .

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PROGRAM SUPPORT AND TASK PLANNING IN REAL-TIME SYSTEMS

The features of real-time systems (RTS), the operation of the RTS, the operating systems of the RT (RTOS) and the development of applications for them; RTS standards; characteristics and analysis of the RTOS; problems of extensions of the RT for Linux and Windows; planning tasks in the RTS, conditions for guaranteed completion of tasks, task parameters; algorithms for static and dynamic planning; criteria/theorems of planning; examples are considered.

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